



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q51885

Akitaka KIMURA

Application No.: 09/161,981

Group Art Unit: 2812

Confirmation No.: 8269

Examiner: Savitri Mulpuri

Filed: September 29, 1998

For: SEMICONDUCTOR LAYER FORMED BY SELECTIVE DEPOSITION AND
METHOD FOR DEPOSITING SEMICONDUCTOR LAYER

AMENDMENT UNDER 37 C.F.R. § 1.114(c)

MAIL STOP RCE

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated December 4, 2003, please amend the above-identified application as follows on the accompanying pages.

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